

About the Program:

Welcome to the **Faculty Development Program on Research Exposure on Signal and Image Processing**. This program is designed to provide educators and researchers with in-depth knowledge and practical insights into the evolving domains of signal and image processing. With rapid advancements in these fields, it has become essential for faculty to stay abreast of emerging techniques and research trends. Throughout this program, participants will explore cutting-edge methodologies, learn how to apply signal and image processing concepts to real-world problems, and discover innovative research directions. Join us in this journey toward enhancing academic and research excellence through the power of signal and image processing.

Eligibility to Participate

- Faculty members from AICTE-approved institutions and universities.
- Research scholars and postgraduate students specializing in Electronics, Electrical, Computer Science, or related disciplines.
- Industry professionals and scientists interested in exploring advanced techniques in signal and image processing.

Participants are expected to have a basic understanding of digital signal or image processing concepts to gain the maximum benefit from the sessions.

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Coordinator(s)

Dr. K Rasool Reddy, Associate Professor, ECE
Dr. S Ravi Kumar, Associate Professor, ECE

Co-Coordinator

Prof. Venkanna Mood, Associate Professor, ECE
Mr. K. Naga Venkateswara Rao, Assistant Professor, ECE

About Program Fees

Research Scholars/ UG or PG Students: 200 Rs/-
Academicians/Scientists: 400 Rs/-

Registration Procedure: Participants interested to attend this program need to make compulsory online registration on the below mentioned link:

<https://forms.gle/yMxFAGEvYxkU2T19>

Payment Details:

UPI ID: rayudu.chinnarao@ibl

Payee Name: Rayudu Chinna Rao

One Week

Faculty Development Programme (FDP)

on

Research Exposure on Signal and Image Processing

24th November – 28th November, 2025

Coordinator(s)

Dr. K. Rasool Reddy

Dr. S. Ravi Kumar

Organized by



Department of
Electronics and Communication
Engineering

St. Martin's Engineering College

UGC Autonomous

Approved by

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About College: SMEC was established in 2002 by St. Martin's Children's Education Society. SMEC offers 6 B.Tech Courses with an intake of 1410 (UG) students per year. SMEC is a prestigious Autonomous (UGC-Govt of India) engineering college and first choice by aspiring students and parents. Since inception, SMEC is functioning with a motto of providing quality education in a highly disciplined and conducive environment with International Standards.

SMEC is awarded with prestigious grade A+ by NAAC. NIRF 2023 ranked, National ranking by ARIIA 2022, 2(f) & 12(B) Recognized by UGC Act of 1956. Only young college in Telangana to receive UGC-Paramarsh, ISO certified, DSIR Recognition, J-Hub certified, TASK certified, Part of Institute Innovation Council (MHRD-Govt of India), Remote center of IIT Bombay, Member of CII & MSME certified Institution. Signed 108 MoUs. Careers 360 Certified as AAA+; Competition Success Review Ranked in top 3; and Wikipedia Ranked 2nd in Telangana.

SMEC is bestowed with the glorious Governor Award Thrice; The Engineering Educators' Award; NIRDPR Award (Govt. of India); IDF Best Partner Award; Dewang Mehta Award; TCS ION Award; CSI Award (Students Chapter); Best Innovation by Federation of Gujarat Industries, Street Cause-Most Dedicated Division, Best college award from Education Matter, Best College in sports facility and achievement by Stumagz, National Leadership Excellence Award by ICCI. Best Engineering College by American College of Dubai. Rs. 21.46 Lakhs received from SERB, Consultancy project worth of Rs. 594 Crores received from GHMC – Hyderabad.

Only college to receive Consultancy work worth of 150 Crores from HMWSSB, Hyderabad. Rs.25 lakhs funding was also received from AICTE.

SMEC students started receiving international awards and funding (4000 USD) from George Mason University Virginia, USA for our best start-up, Rs.1.3 crores funding received from MSME.

The remarkable achievement by the faculty members of the college is that they have published 300+ books, 12886+ research papers, 1000+ patents, 108+ copyrights and 50,000+ international certification courses. The institution has signed more than 108 MoUs with worldwide major companies and institutions. The crowning glory in academic excellence was achieved by bagging gold medals every year. 138 innovative products are developed by students and faculty members. SMEC has a strong vision of offering world class training to the promising engineers and Management professionals. SMEC is situated in an eco-friendly environment; the college has the best infrastructure. 100% ragging free campus.

About the Department of ECE: Electronics and Communication Engineering, the evergreen and always blooming field of study was established in the academic year 2002-03 with an intake of 60 and the present intake is 240. We are technologically updated and follow the strict curriculum provided by JNTUH to impart a standard education to our students. There are 43 well qualified and trained faculty from various reputed universities and institutions. Currently there are 5 Professors, 8 Associate Professors, and 30 Assistant Professors in the department who nurture the students. The Department is accredited by NBA 5 times since 2008. Department of ECE faculties published 621 research papers, published 20 books and filed 30 patents. Students are also participated 40 events and bagged awards in all events. Third year student received 4000 dollars from Goerge Manson University, Virginia USA.

Salient Features of the Department of ECE

- ✓ 5 times NBA Accredited.
- ✓ ISO 21001:2018 Certifications.
- ✓ NAAC A+ Grade.
- ✓ MOUs with 05 Industries.
- ✓ Research Development Activities.
- ✓ Club Activities.
- ✓ Mentor-Mentee System.
- ✓ Department Library.
- ✓ Well Equipped Laboratories.
- ✓ High end Signal & Image Processing Laboratory.
- ✓ IoT Laboratory.
- ✓ Round the Clock Online Resource Library.
- ✓ Smart classroom.
- ✓ 100 % placement with highest package of Rs 58 Lakhs per annum in Paloalto.

Resource Persons

1. Dr. Durga Prasad Bavirisetti
Associate Professor,
Department of Computer & Geospatial Sciences,
University of Gavle, Sweden
2. Dr Vijayakumar Kadha,
Assistant Professor,
Department of Electronics & Communication
Engineering,
Amrita Vishwa Vidyapeetham, Amaravati,
Andhra Pradesh
3. Dr. Kandala N V P S Rajesh
Associate professor,
School of Electronics Engineering,
Vellore Institute of Technology, Andhra Pradesh
4. Dr. Madhu Oruganti
Assistant Professor,
Department of AI & DS,
KL University, Telangana
5. Dr. Lakshminarayana Janjanam
Assistant Professor, Senior Grade 1,
School of Electronics Engineering,
Vellore Institute of Technology, Andhra Pradesh

One Week Faculty Development Programme (FDP) on Research Exposure on Signal and Image Processing

24th November – 28th November, 2025

Department of Electronics and Communication Engineering

St. Martin's Engineering College

SESSION SCHEDULE

Day 1 24/11/25	Day 2 25.11.2025	Day 3 26.11.2025	Day 4 27.11.2025	Day 5 28.11.2025
6:00PM – 6:30PM Inaugural Session	6:30PM – 8:00PM Dr. Kandala N V P S Rajesh	6:30PM – 8:00PM Dr. Vijayakumar Kadha	6:30PM – 8:00PM Dr. Madhu Oruganti	6:30PM – 8:00PM Dr. Duraga Prasad Bavirisetti
6:30PM – 8:00PM Dr. Lakshminarayana Janjanam Session Title : Meta-ID: Intelligent System Identification	Session Title: Signal Intelligence: Advancing Machine and Deep Learning Approaches for Robust Signal Understanding and Classification.	Session Title: Digital Forensics: Towards Effective Deep Learning Methods for Image Authentication.	Session Title: Age Invariant Kinship Verification: Analysis And Solutions.	Session Title : Machine Learning in Action: How AI Detects Snow Poles to Localize a Vehicle in Limited GNSS under Harsh Nordic Weather Conditions